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Functional Analysis of Battery Management Systems using Multi-Cell HIL Simulator

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Abstract—Developers and manufacturers of Battery Management Systems (BMSs) require extensive testing of controller HW and SW, such as analog front-end (AFE) and performance of generated control code. In comparison with tests conducted on real batteries, tests conducted on hardware-in-the-loop (HIL) simulator may be more cost ant time effective, easier to reproduce and safer beyond the normal range of operation, especially at early stages in the development process or during fault simulation. In this paper a li-ion battery (LIB) electro-thermal multi-cell model coupled with an aging model is designed, characterized and validated based on experimental data, converted to C code and emulated in real-time with a dSpace HIL simulator. The BMS to be tested interacts with the emulated battery pack as if it was managing a real battery pack. BMS functions such as protection, measuring of current, voltage and temperature or balancing are tested on real-time experiments.

Keywords—battery management system; hardware-in-the-loop; lithium ion.

I. INTRODUCTION

A well-known limitation of LIBs is the need of reliable electronic protection, i.e. a BMS, to avoid electric or thermal abuse. The reason for this is to minimize battery degradation and to prevent thermal instability, conditions that can lead certain chemistries to a thermal runaway.

Moreover, in order to maximize battery capacity, active or passive balancing circuits are implemented in BMSs. Without balancing, if multiple cells are connected in series, the weakest cell constraints the entire pack performance. Local operational differences and uncertainties at the material level are related to these cell-to-cell variations, which lead to further differences in the cell-to-cell performance over time.

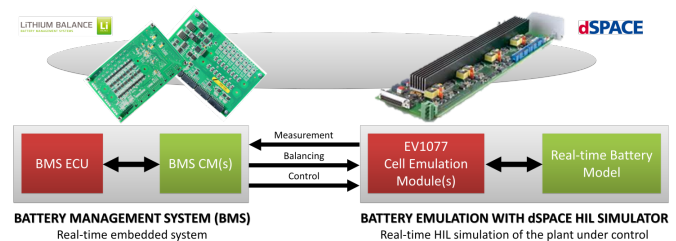


Fig. 1: Concept of high-precision cell voltage emulation.

In this paper a LIB electro-thermal multi-cell equivalent circuit model (ECM) coupled with an aging model is designed, characterized and validated based on experimental data, converted to C code and emulated in real-time with a dSpace HIL simulator (see Fig.1 and Fig.2). The HIL simulator is able to provide signals for testing a wide variety of BMS functions, such as cell protection; safety validation; measuring of current, voltage and temperature; balancing of cells; analysis and parameter prediction; logging, telemetry and external communications.

The paper is organized as follows. In Section II the concept of battery emulation is reviewed based on literature. In Section III the proposed electro-thermal LIB pack model is presented and parameterized based on experimental data from large format NMC Li-ion pouch cells. In Section IV the commercial BMS under test is briefly described. In Section V experimental results of the interaction of the BMS with the emulated battery pack are provided. Finally, Section VI gives the conclusions.

II. CRITICAL REVIEW OF BATTERY EMULATION

Nowadays HIL simulators are widely employed to prototype, design and test complex real-time systems in many applications due to a combination of safety, feasibility, time and cost effectiveness factors. In the next

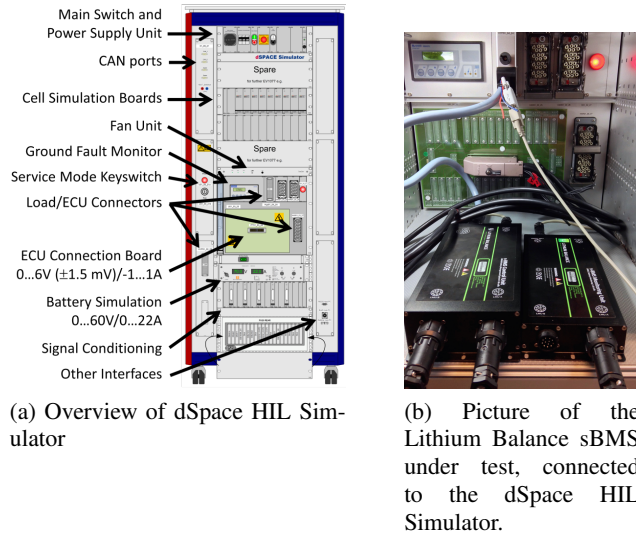


Fig. 2: Setup of Hardware-in-the-Loop test bench.

section, the HIL simulation approaches in relation with LIBs presented in scientific and technical literatures are reviewed with a special focus on automotive applications.

HIL simulation uses some virtual or emulated components and some physical components. If a real LIB system is used, the method is so called battery-in-the-loop (BIL) or battery-hardware-in-the-loop (BHIL). BIL simulation is proposed in automotive applications for the purposes of battery evaluation [1], BMS validation [2], verification of battery models [3] or assessment of power or energy management strategies [4]-[6], considering a complete virtual vehicle [1]-[4] or in combination with other real components [5],[6].

HIL simulation methods with virtual LIBs, pack-based (or aggregated) approaches [7]-[22] are more frequent in literature than cell-based (or multi-cell) approaches [23]-[32]. In general, in order to control the charging/discharging process, any BMS for a series or series-parallel connected battery pack should at least monitor the pack voltage, the pack current, the single cell voltages and the temperatures at cell or pack level. Moreover, cell balancing is required to avoid severe state of charge unbalances over time due to cell-to-cell differences [33]. Hence, it is easy to understand that, for purposes of complete BMS validation, cell-based HIL simulation is preferred over pack-based.

HIL simulation using virtual LIB pack-based methods is proposed in stationary [20]-[22] and automotive applications [7]-[19], the latter with the goal of electric powertrain evaluation [7]-[11], BMS validation [12],[13], vehicle controller unit testing [14], assessment of power or energy management strategies [15],[16] or verification of the feasibility of the HIL concept [17],[18]. On the other hand, HIL simulation using virtual LIB cell-

based methods is proposed either for addressing BMS validation [23]-[31] or only testing balancing circuits [32].

If there is high power flow between real components and the HIL simulator, the technique is called power-hardware-in-the-loop (PHIL) [7]-[11], [15], [16], [18]-[20]. This is a common feature of pack-based HIL simulators used for electric powertrain evaluation [7]-[11]. PHIL simulation is also applied in [19] to lead-acid technology, where a portable system using virtual 12V SLI battery is presented and tested during motor cranking in a car.

In contrast, the usual case in HIL simulation only involves low level of power, voltage and current related with analog and digital I/O signals or communication networks, e.g. CAN bus [1]-[6], [12]-[14], [17], [21]-[32]. In general, this is the case of all the cell-based HIL simulation approaches found in literature [23]-[32]. In some cases a strictly communication based approach is proposed [14], [21]-[25], [29], which means that BMS functions such as measuring or balancing cannot be tested, either at cell or pack level.

Regarding balancing, an isolated power interface for each cell in series (or group of cells in parallel in case of a series-parallel connected pack) is desired to exchange bi-directional power flow between virtual cells and real electric circuits. But such interface is only presented in cell-based simulators described in [30]-[32]. Results are shown, respectively, for only 1, 4 or 6 cells in series, and current levels at 200 mA, 1.45 A, or 5 A. Moreover, it should be noted that the setup presented in [32] is only targeted for testing balancing circuits, and not for testing any other BMS functions.

With regards to the temperature emulation, it is claimed in several references, but the thermal model is either not described [12], [13], [17], [23], [26] or the temperature is artificially introduced [24]. In fact, a thermal model is only presented in [18]. However, a pack-based approach is followed and the lumped equivalent circuit model described is parameterized without experimental data. Moreover, the self-made HIL simulator setup presented can only run discharging profiles.

The most common approach for battery modelling methods are electrical models based on ECMs, e.g. impedance-based modeling (abstract approach) [3], [7]-[13], [16], [18]-[21], [25], [26], [30]-[32]. Other approaches are proposed rarely in the literature. ECMs can provide information about the macroscopic quantities that are monitored by a BMS, i.e. current, voltage and temperature (if a thermal model is included), offering a better trade-off between complexity and accuracy than other mathematical or physic-chemical approaches, like

the ones proposed in [7] and [15]. In [7] an extended modified Shepherd model (abstract approach) is proposed, and in [15] a Dual-foil based-model (physico-chemical approach).

However, ECMs do not allow extrapolation from one material or cell designs to another one. Therefore, for a universal BMS validation, a library of battery models for different cell sizes, formats, chemistries and manufacturers are desired. Such a library is not presented in the reviewed literature [1]-[32].

Moreover, the accuracy of the ECM lays on the circuit topology and the component dependencies on state-of-charge (SOC), state-of-health (SOH), current or temperature. The common approach is to use non-linear dynamic ECMs with two RC elements. This circuit topology may account for most important dynamic effects. However, simple or no dependencies are usually considered in literature. For example, only SOC dependencies in open circuit voltage are considered in [8], [10], [20], [21], [25], [31] and the dependencies are not considered or are not well defined in [9], [11], [12], [17], [18], [23], [24].

It should be noted that even if a non-linear dynamic ECM with two RC elements is used, the model may not reproduce accurately realistic battery profiles if only a few or non-dependencies are considered in the circuit components.

Furthermore, in order to account for cell-to-cell differences or emulating different testing scenarios, an aging model may be coupled to the electrical model. This approach is not followed in the reviewed literature [1]-[32]. It should be noted that, for purposes of general functional analysis of a BMS, simpler battery models may be used. However, more accurate models may be implemented with the aim of ensuring that complex functionalities of a BMS, such as diagnostic algorithms for State-of-Charge (SOC), State-of-Function (SOF) or State-of-Health (SOH) estimation, can be analyzed and validated using HIL simulation.

III. ELECTRO-THERMAL LIB PACK MODEL

In this section the LIB electro-thermal multi-cell ECM model coupled with an aging model is designed and characterized for a commercial 40 Ah high-energy Kokam SLPB100216216H pouch cell. The ECM model consists of an inductance, an ohmic resistor and two ZARC elements in series. Experimental pulse power characterization, capacity check, electrochemical impedance spectroscopy (EIS) and anisotropic thermal diffusivity measurement techniques are used for parameter estimation, taking into account aging, temperature and SOC dependencies.

Results are presented for a Kokam cell for illus-

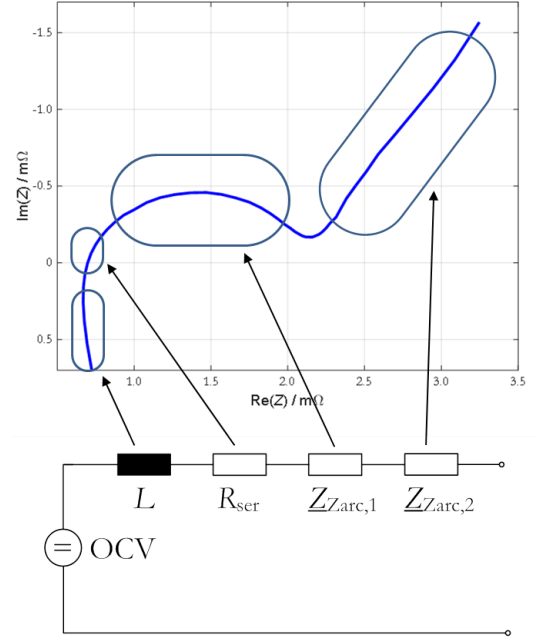


Fig. 3: Mapping of processes from the impedance spectra to equivalent circuit model.

trative purposes, but a full library has been developed including battery cells with different formats (cylindrical, pouch, prismatic), capacities (2Ah, 53Ah, 100Ah) and chemistries (NMC, LTO, LFP), including manufacturers such as Sanyo, Panasonic and SkyEnergy.

A. Impedance spectrum analysis

In Fig. 4, EIS measurements conducted on the Kokam 40 Ah NMC-cathode cell are presented in a Nyquist plot. At high frequencies (> 0.8 kHz) the cell shows inductive behavior caused by inductive reactance of metallic elements in the cell and wires. This is modeled as an inductor in series in the ECM. At a frequency of 375 Hz pure resistive behavior is observed, related with the sum of the ohmic resistances of current collectors, active material, electrolyte and separator. This effect is modeled as a resistor in the ECM. At lower frequencies, one depressed semi-circle is observed in the spectrum. This is associated to a SEI effect superimposed with the double layer capacity and the charge-transfer resistance at the electrodes. All these effects can be modeled by using a ZARC element in the ECM. At frequencies below 0.375 Hz a linear slope with an angle of 45° is observed. This is associated with the diffusion process and can be also approximated by another ZARC element.

A ZARC element is made up of a resistor in parallel

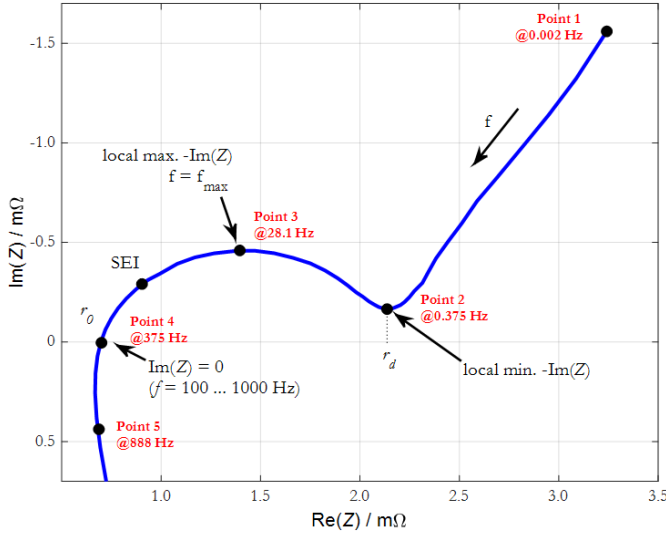


Fig. 4: Nyquist plot of the complex impedance of a Kokam 40 Ah NMC-cathode cell.

with a constant-phase-element (CPE):

$$\omega_0 = \left(\frac{1}{A \cdot R} \right)^{\frac{1}{\xi}} \iff \underline{Z} = A \cdot (j\omega)^{-\xi} \quad (1)$$

$$\omega_{0,ZARC} = \omega_{0,RC} = \frac{1}{C_{RC} \cdot R} \quad (2)$$

$$\iff C_{RC} = \frac{1}{\omega_{0,RC} \cdot R} \quad (3)$$

A CPE consists of a generalized capacity and depression factor. If the depression factor is equal to 1, the ZARC element corresponds to a simple RC element. If it is equal to 0 it just represents a resistor in series.

B. Approximation of ZARC elements

ZARC elements are able to accurately reproduce the impedance spectra of LIBs, but it is impossible to transfer without approximations their parameters from the frequency domain into the time domain. A common method to approximate ZARC elements is using a variable number of RC-elements (Fig. 5). Each of the individual RC elements represents a semicircle in the Nyquist plot. To represent the ZARC exactly, infinite RC elements are needed. Nevertheless, taking into account the computational cost vs. accuracy, a few RC elements are sufficient for a good approximation.

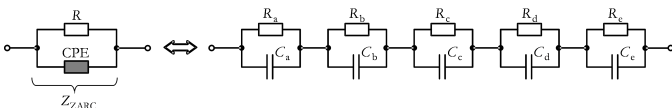


Fig. 5: Approximation of ZARC element through series connection of RC elements [34].

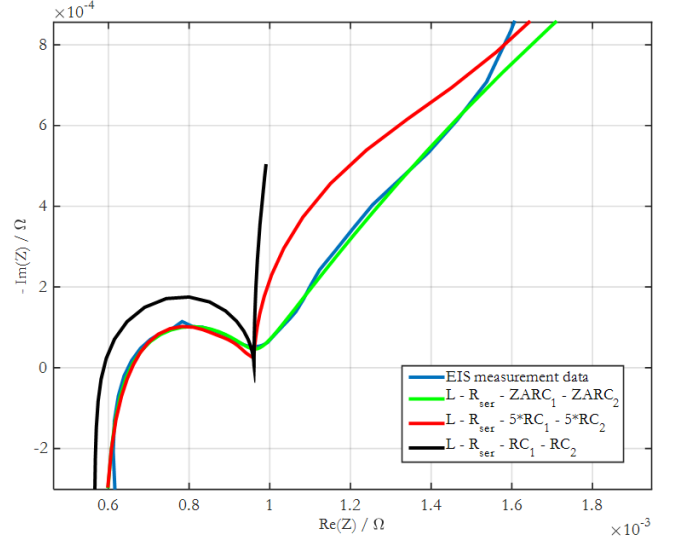


Fig. 6: Comparison of different model approximations.

As observed in Fig. 6, the error for 5 RC elements in comparison to the approximation with only 1 RC element is reduced to a satisfactory result. The overall approximation equation is derived as following [34]:

$$\underline{Z}_{2ZARC}(j\omega) = R_{ser} + \frac{R_1}{1 + (j\omega)^{-\xi} \cdot R_1 \cdot C_1} + \frac{R_2}{1 + (j\omega)^{-\xi} \cdot R_2 \cdot C_2} \quad (4)$$

C. ECM parameter identification

Some parameters of the ECM can be directly derived from the impedance spectrum. For example, the series resistance at high frequencies can be estimated from the intersection of the spectrum with the real part axis. Other parameters, like the inductor in series, can be neglected in some cases [35]. For example, if electric vehicle applications are simulated, due to their characteristic slow dynamics. Moreover, it does not make sense to simulate phenomena that cannot be measured due to limited sampling rate of the BMS or the HIL simulator.

An in-house tool based on Matlab Easyfit function is applied to fit the experimental EIS data in order to parameterize the ECM. The Nelder-Mead simplex search algorithm is used, which allows an accurate and precise emulation of the impedance spectra based on the modeling function. This function is crucial, because it defines the quality and robustness of the parameter identification process. It should be noted that it is always useful to define starting parameters derived directly from the impedance spectrum [36].

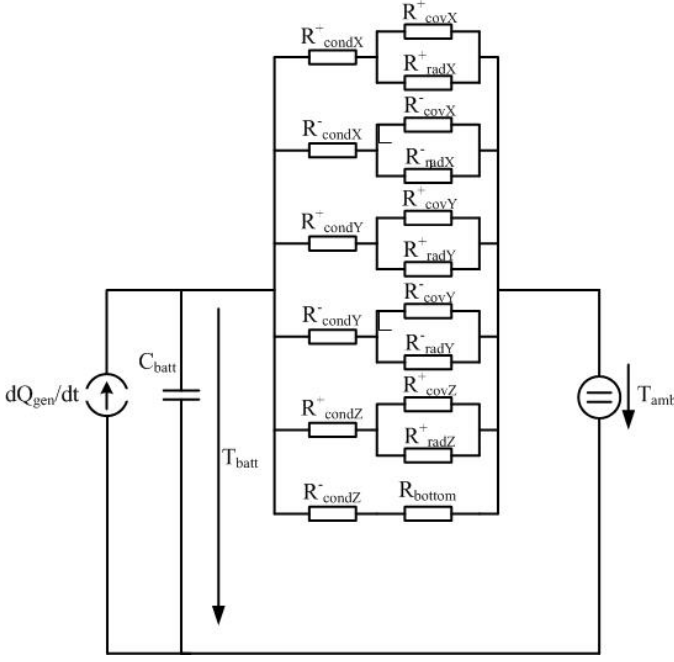


Fig. 7: Employed equivalent circuit for thermal modelling.

D. Temperature model

Thermal effects modelled at cell level using an ECM, as illustrated in Fig. 7. The underlying processes such as heat flow dQ/dt or temperature are converted to current and voltage. Therefore, radiation, convection and heat conduction are represented by a voltage drop over non-linear thermal heat transfer resistances as:

$$R_{\text{radiation}} = T_1 - T_2 / (A \cdot \alpha_{1,2} \cdot (T_1^4 - T_2^4)) \quad (5)$$

$$R_{\text{convection}} = 1 / A \cdot \alpha \quad (6)$$

$$R_{\text{conduction}} = d / \lambda \cdot A \quad (7)$$

with d representing the thickness, λ the specific heat conductivity and α the convection coefficient.

The temperature of a volume element is calculated using the current temperature and its change. The temperature of an element is increased by the temperature difference ΔT , once a heat ΔQ is applied. The heat is proportional to ΔT :

$$\Delta Q = C \cdot \Delta T \quad (8)$$

C represents the heat capacity of the element and depends on the material and mass. ΔT is calculated by:

$$\frac{dT}{dt} = \frac{1}{C} \left(\frac{dQ_{\text{received}}}{dt} - \frac{dQ_{\text{delivered}}}{dt} \right) \quad (9)$$

At battery pack level the following heat transfer phenomena are considered:

a. transfer within a cell (mostly heat conduction)

- b. transfer to the environment (convection and radiation)
- c. transfer to neighbouring cells (heat conduction, similar to a.)

The heat conductivity is defined by:

$$\frac{dT}{dt} = \frac{1}{C} \cdot \sum (-T - T_i / R_i) \quad (10)$$

with index i describing the thermal resistance R_i and conductivity of the neighbours. The heat exchange with the environment is based mainly on the convection and radiation and described as:

$$\frac{dT}{dt} = \frac{1}{C} \cdot \sum \left(\frac{-dQ_{\text{conv}}}{dt} - \frac{dQ_{\text{rad}}}{dt} \right) \quad (11)$$

with

$$\frac{dQ_{\text{conv}}}{dt} = \alpha_{\text{con}} \cdot A \cdot (T - T_i) \quad (12)$$

$$\frac{dQ_{\text{rad}}}{dt} = \alpha_{\text{con}} \cdot A \cdot (T^4 - T_i^4) \quad (13)$$

The thermal parameters were extracted using a non-destructive evaluation of the battery by anisotropic thermal diffusivity measurement by means of thermography. After exciting the front plane of the battery with a flash lamp (in-plane measurement) or induction coil (through-plane measurement) the rise of temperature in the rear face plane is recorded by a thermal imaging camera. The extracted parameters for the Kokam cell are presented in Table I.

TABLE I: Thermal parameters extracted by anisotropic thermal diffusivity measurement

heat conductivity in-plane	33	$W/(m \cdot K)$
heat conductivity through-plane	0.61	$W/(m \cdot K)$
Density	1.982	$kg/(dm^3)$
Heat capacity	860	$J/(kg \cdot K)$
Mass	1.18	kg

E. Aging model for HIL simulation

In order to emulate the variation of cell-to-cell characteristics the impedance-based ECM is coupled to an aging model. This model maps the results of the parameterization from accelerated aging tests of battery cells cycled with a real driving cycle. A physical based function is extracted from the experimental data in order to describe the aging state of the cell. All the cycling tests were carried out at different temperatures based on the average yearly temperatures of Germany. The increase of internal resistance and loss of capacity are monitored every 100 driving cycles conducting checkup or reference performance tests.

The checkup tests consist of a pulse power characterization test for current dependency investigation and

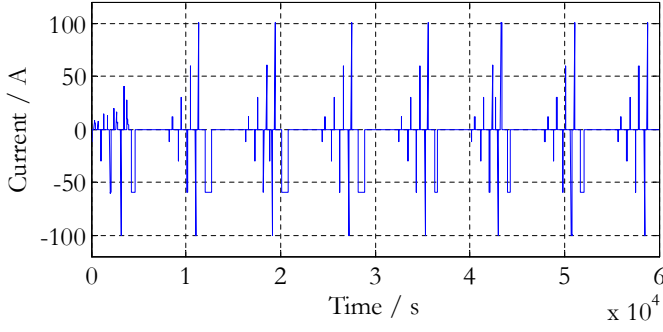


Fig. 8: Pulse power characterization profile.

a capacity check. The pulse power characterization test is intended to determine the internal resistance over the cells usable voltage range using a test profile that includes both discharge and charge/regen pulses. The test profile in Fig. 8 is applied to the cell under test for different temperatures in consecutive steps of 10% SOC. The SOC is adjusted based on the accumulated Ah referring to the nominal capacity. The resistance is calculated as the difference between the voltage before the discharge pulse and Δt after the beginning of the discharge pulse, divided by the amplitude of the current. The capacity check tests consist of a typical CCCV charge and CC discharge cycle conducted at 1 C-rate and different temperatures. It should be noted that during the checkup tests the cells are operated always inside a safe operating area limited by certain temperatures, voltages and currents specified in the manufacturer's data sheet.

Finally, the ZARC element parameters are identified based on EIS measurements. The impedance spectra are also measured at different SOC and temperatures for using frequencies between 10 mHz and 5 kHz.

To represent the individual aging states of the cells, three parts are integrated as shown in Fig.9:

- 1) Impedance-based electric model
- 2) Thermal model
- 3) Aging simulation

Every aging parameter was extracted separately and can be changed offline or on-the-fly while running a HIL simulation. Hence, it is possible to represent realistic operating conditions of multi-cell battery packs by applying a statistical distribution of the individual aging states. Therefore, using the three part model proposed here, a full range of realistic scenarios can be addressed. This means that the functional safety of the system can be verified in an early development stage.

IV. COMMERCIAL BMS UNDER TEST

The commercial BMS under test is the sBMS v6 provided by Lithium Balance A/S. The BMS uses a

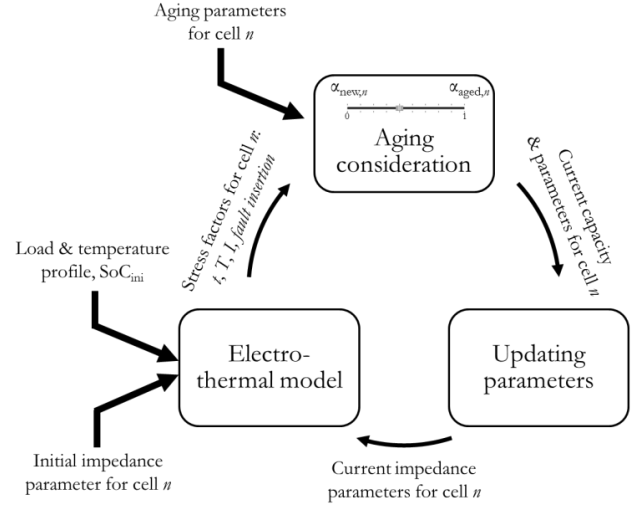


Fig. 9: On-line integration of individual cell aging simulation with electro-thermal model.

master-slave configuration, where each slave unit can be configured to supervise 8 cells and 2 temperatures. The slave is capable of passive balancing with up to 1 A simultaneously on all 8 cells and will automatically reduce the balancing power if the heat cannot be dissipated at high enough rates. A total of 32 slave units can be used for each master, giving a total of 256 supervised cells and 64 temperature inputs. The master is capable of measuring LIB pack current, LIB pack voltage, isolation faults, and contactor faults. Communication to external systems is provided through a configurable CAN bus interface, where any parameter can be broadcasted. The control of contactors, switches and fans are all implemented in the BMS and can have a multitude of configurations.

V. EXPERIMENTAL RESULTS

In this section, the experimental results resulting for HIL tests of the commercial BMS are presented. The testing performed on the HIL setup and the commercial BMS comprises: cell voltage and pack current accuracy, temperature tracking, cell balancing and fault insertion.

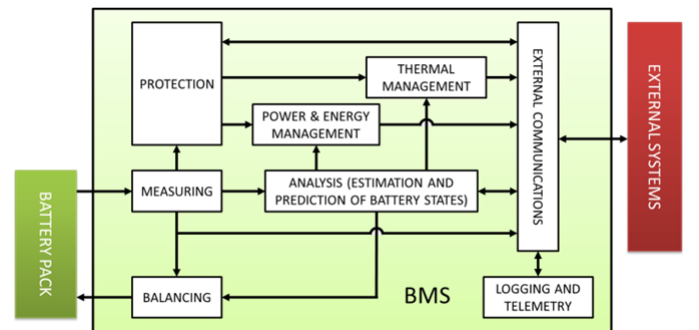


Fig. 10: Overview of typical BMS functions.

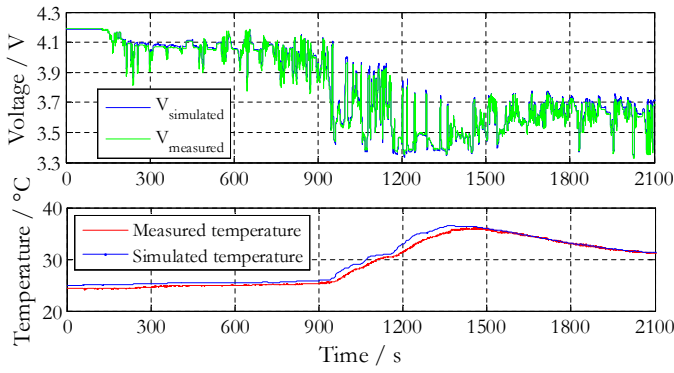


Fig. 11: Electro-thermal battery model validation between measurement data and model simulation.

A. Model Validation

As a first step, the electro-thermal model is validated in simulations. Fig. 11 shows the simulated voltage response in comparison to the real measured voltage. The dynamic load profile applied was logged in a real EV and processed for simulation. It can be noticed that the model is able to follow the measured voltage response. The same can be concluded from the temperature comparison. The starting temperature is initialized at 25°C and stays constant due to lower dynamical driving. As soon as the load current increases, a rise in temperature is observed due to higher power losses. The model is also able to follow this trend accurately. Therefore its applicability for HIL simulation is demonstrated. or State-of-Health (SoH) estimation, can be analyzed in future tests.

B. Validation of BMS with HIL emulation

In the next step, the model-based battery simulation is converted to C-code and emulated in real-time on the HIL test bench. Therefore, communication interfaces between the HIL and the BMS are defined and implemented. Fig. 12 shows the emulated cell voltages for 32 cells connected in series. The entire scenario aims to represent a typical driver behavior, where a standard user drives to work with a fully charged vehicle. After arriving at the workplace the car should not be charged immediately, since higher SOC levels during OCV conditions may accelerate aging. It is assumed that the BMS is able to calculate the charging time and only starts the charging procedure when it is required according to the driver usage patterns. The dynamic characteristics such as initial impedance parameters, initial cell temperature and initial ambient temperature are selected assuming that the underlying distribution is normal. Parameters can be changed off-line or on-the-fly during emulation. An exemplary charging scenario is illustrated in Fig. 13. Different initial voltages and cell-to-cell characteristics

are considered at the beginning of charge. These voltage differences are equalized by the BMS balancing circuits during the charging process, in order to maximize usable battery capacity. The parameters for the balancing window are defined in the GUI of the commercial BMS. After the BMS initiates the charging process, the voltage increases gradually ($t = 5500s$). As soon as one cell reaches the cell target voltage, the balancing is activated as illustrated in Fig. 13.

Due to balancing, the overshoot is reduced within the allowable window. All the cells, even though having different characteristics and initial voltages are balanced to the cell target voltage at the end of charge.

C. Fault insertion

When testing a system for its functional requirements, it is important to proof the system for every possible type of fault scenarios and errors. Only a successful pre-testing activity helps to identify potential failure modes events during the battery pack lifetime. Utilizing FMEA at the start of the developing process, which carries out a systematic and structured evaluation, helps to determine the consequences of failure mechanisms. The HIL simulator has to provide all the relevant signals to test and evaluate all safety related functions, including:

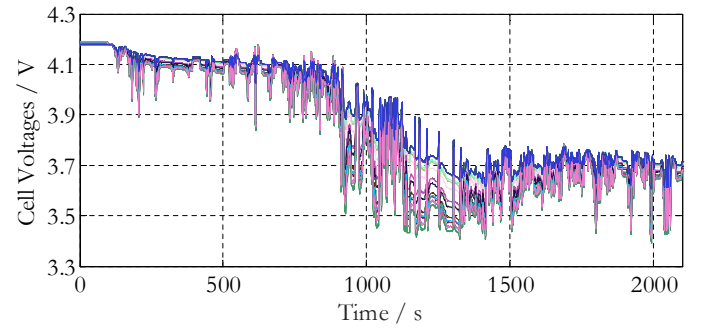


Fig. 12: HIL battery cell emulation using dynamic driving cycle.

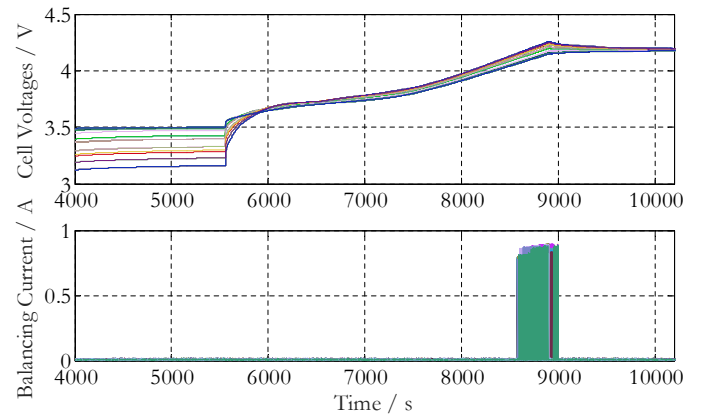


Fig. 13: HIL battery cell emulation during charging.

- 1) Individual cell and pack voltage
- 2) Battery current
- 3) Internal BMS Communication
- 4) Safety signals (contactor control)
- 5) Diagnostic signals

Mainly two possible causes for error in the system need to be addressed. First, hardware related errors on the master or slaves of the BMS. Second, cell-based errors like problems in electric connection due to corrosion, intercell-connection or even total failure of the wiring harness.

Fig. 14 illustrates a possible failure caused by a higher internal resistance and self discharge or cell interconnection problems. In order to emulate this fault, additional resistances are added to the cell single model during operation. The figure shows the dynamic response to a load profile recorded in an electrical vehicle.

With the aim of representing realistic operating conditions, a statistical distribution of the individual aging states is applied to the 32 cells in series considered in the setup. At high levels of discharge or charge/regen power, the effects of this cell-to-cell parameter variation are evident.

After a simulation time of 1000 s, the failure mode is applied and within a short time the voltage of cell 1 (blue curve) tends to diverge from the average. Additionally, due to higher resistance in cell 1, the temperature is rising to a higher level in comparison to the healthy cells. Such a scenario has to be detected by the BMS and counter measurements have to be performed, either by BMS or the energy management of the vehicle supervising unit. Complex algorithms computed on a BMS master unit may be able to detect the problem. However, simpler algorithms (e.g. cell voltage average) implemented either at master or slave level may also be able to detect the fault, providing a higher degree of redundancy.

Another possible fault mode considers the interconnection error between the BMS slave board and the terminals of the cell. Vibration, wiring error and blown fuses may lead to a voltage sensor reading of 0 volts.

Then, since the controller is not able to measure the cell voltage, the BMS goes into a critical fault mode. Depending on the configuration settings of the BMS controller, it may not allow to further charge/discharge the pack. This failure mode is simulated by activating a relay while running the HIL emulation. Fig. 15 shows this scenario for a load driving profile with 32 cells in series. After 950 s, the cell voltage of cell 12 (red signal) suddenly drops to zero volts due to fault insertion.

The same kind of test can easily be performed for the temperature sensors, however this fault might not lead to

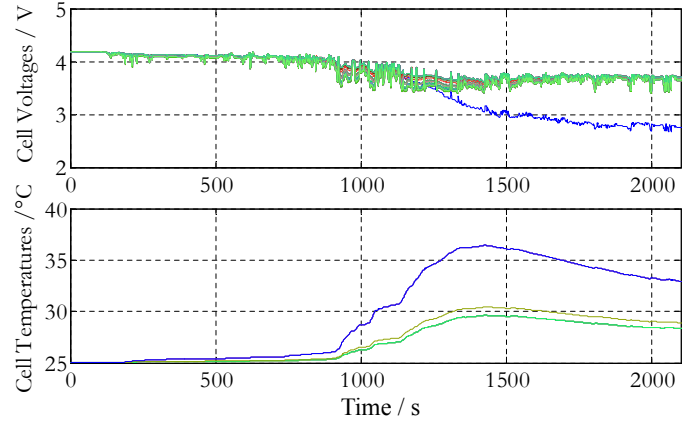


Fig. 14: Model-based fault insertion: Higher series resistance for cell 1.

a critical fault mode.

Since the HIL setup allows for different battery pack topologies, it is also possible to test a multitude of serial, parallel or mixed serial-parallel connections of cells. This is important for testing battery packs consisting of multiple modules and how a BMS handles scenarios related to modules failing or being replaced.

VI. CONCLUSION

This paper presents a state-of-the-art HIL simulation of a commercial BMS. In comparison with test conducted on real batteries, BMS validation tests conducted on HIL simulators may be more cost and time effective, easier to reproduce and safer beyond the normal range of operation, especially at early stages in the development process or during fault simulation.

The methodology followed in this work comprises following stages: (1) comprehensive review of battery simulation approaches (2) development of an advanced multi-cell impedance-based model (3) development and coupling of an aging model (4) model validation in

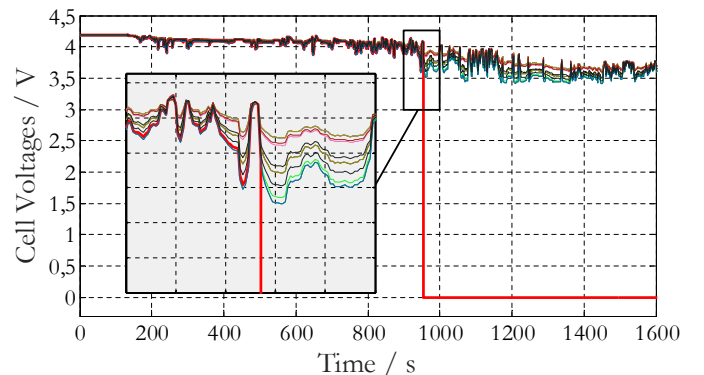


Fig. 15: Model-based fault insertion: Critical zero volt mode.

simulations (5) validation of commercial BMS with HIL emulation (6) fault insertion. For purposes of general functional analysis of a BMS, simple battery models are commonly used in HIL simulation. However, complex models are needed to ensure that all the functionalities of an advanced BMS can be analyzed and validated, e.g. diagnostic algorithms for SOC, SOF or SOH estimation. In this paper, a non-linear dynamic electro-thermal multi-cell ECM coupled with an aging model is presented, taking into account temperature, aging and SOC dependencies. Parameterization is based on experimental data from tests conducted on a commercial 40 Ah high-energy Kokam SLPB100216216H pouch cell. The testing procedures comprised EIS measurements, pulse power characterization tests, capacity checks and anisotropic thermal diffusivity measurements at different aging states. The model is validated in simulations using a dynamic load profile from a real EV. Finally, BMS validation with a HIL simulation is presented. The tests performed on the HIL setup comprised: cell voltage and pack current accuracy, temperature tracking, cell balancing and fault insertion.

Future work will address the following topics: influence of battery models, validation of advanced BMS diagnostic algorithms and a thorough assessment of error and advanced fault insertions.

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